

**REMARKS**

Applicants thank the Examiner for acknowledging receipt of Applicants' foreign priority document that has been submitted pursuant to 35 USC section 119. Applicants also thank the Examiner for acknowledging acceptance of the drawings.

Applicants respectfully request reconsideration of the prior art rejections set forth by the Examiner under 35 USC §§102 and 103. Applicants respectfully submit that the prior art references of record cited by the Examiner fail to either teach or suggest Applicants' presently claimed invention. More specifically, Applicants' claimed invention is directed to an improved protection circuit for a semiconductor device.

In particular, Applicants note that United States patent No. 4,760,434 is merely directed to a semiconductor device for protecting against overheating. Specifically, this reference only discloses an MOS protection circuit and device in which there are diode pairs connected back-to-back. Significantly, there is no disclosure whatsoever regarding the multiple pairs of diode's as described and claimed in the instant application. The same distinctions apply to the other reference, United States patent No. 6,433,985. This reference is very similar to Applicants' admitted prior art in figure 7. There is simply no teaching or suggestion whatsoever in any of the references of record regarding the multiple pairs of diodes as disclosed and claimed herein.

In light of the foregoing, Applicants respectfully request that the Examiner now withdraw the rejections and allow all claims in the application.

Respectfully submitted,



(Reg. #37,607)

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